Remarks

The present amendment is responsive to the office action mailed Jan 16, 2008.

Drawings: With regard to the drawing objections, applicant has amended the drawings as follows:

Reference "204" has been amended on Figure 3A to "202" High Speed Bus, thereby matching the labeling of figure 3B and the amended paragraph starting on page 8 line 23 of the specification.

Reference 126 is correct on drawing figure 2, but was incorrectly referenced as 126 in the specification page 8 line 1, which is amended to "128" in the amended paragraph which starts on page 6 line 18.

References "200" and "202" of figures 3a and 3b indicate the contents of low speed bus 123 and high speed bus 119, respectively shown in figure 2. The paragraph starting on page 8 line 23 is amended to include these references.

References to "high speed data bus" and "low speed data bus" are amended to "high speed bus" and "low speed bus" respectively on the paragraph starting on page 6 line 18.

Figure 2 is amended to change "Pwr_up" to --power-on-- with reference numeral 131 to match the specification.

Figure 5 adds "Y" to decisions 504, 516, 518, and "N" to decisions 516 and 518 to match the specification.

With the drawing changes and specification amendments, no new matter is entered into the specification.

Specification:

With regard to the objection of the specification for "low speed data bus 123", applicant has amended the paragraph starting on page 6 line 18 to recite --low speed bus 123--

With regard to the objection of the specification for "high speed data bus 119", applicant has amended the paragraph starting on page 6 line 18 to recite --high speed bus 119--

With regard to the objection for "wireless host 126" on page 8 line 1, applicant has amended this paragraph to show "wireless host 128".

With regard to the objection of claim 1 "said ROM data", applicant has amended this claim to remove a reference to "said ROM data", referencing "said boot image" which is stored in the ROM.

With regard to the 35 USC 103(a) rejection of claim 1, over Lachance 7,017,038, Olson 6,324,598, and Chang 2003/0126242, applicant notes that the system of LaChance initializes from a "bootstrap memory used to store a bootstrap program" (col 1 lines 58-60), whereas the system of claim 1 initializes from three values - a SRC value, a DST value, and a LENGTH value. These three values and a boot image are copied into memory, after which the CPU begins executing. In the system of LaChance, the CPU begins execution immediately on the bootstrap program (col 1 lines 63-67). This is not possible in the present invention, as the CPU executes a program stored in memory which program is not present until the three values are loaded into the DMA controller, which moves an image from ROM to memory. Olson describes a DMA controller which acts to offload data movement tasks of a microprocessor (col 1 lines 17-21) which is already executing instructions. Olson does not teach the use of a DMA controller for movement of the instructions such as a boot image which a

CPU requires, as the CPU cannot execute from un-initialized memory before the boot image is moved into this memory. The initialization of the DMA controller of claim 1 occurs before the CPU has a program loaded to execute. Therefore, the DMA controller of claim 1 is initialized by the ROM controller, function which is not present in Olson, since the DMA controller of Olson is initialized by the CPU, which is already running (col 1 lines 48-50). The structure and function of claim 1 prohibits the initialization of the DMA controller by the CPU, which is inactive during this DMA controller initialization. The wireless receiver of Chang discloses a protocol similar to BootP as described in applicant's prior art, but does not describe the initialization of the DMA controller using a ROM controller which initializes memory by instructing the DMA controller to move an executable image into a memory based on a SRC, DST, and LENGTH value moved from a ROM into a DMA controller by a ROM controller. Therefore, Lachance, Olson, and Chang cannot be combined to anticipate amended claim 1, as the ROM controller of claim 1 which initializes the DMA controller is absent in these references, and neither the ROM of LaChance or Olson, nor the system described in Chang, is capable of operating in the manner indicated in claim 1, as these references require a CPU

running and memory initialized with an executable program to perform the operations described in claim 1. Applicant additionally notes that the ROM of claim 1 is not coupled to the CPU as in LaChance and Olson.

With regard to the 35 USC 103(a) rejection of claim 3, 5, 6, 7, applicant notes that these are proper dependant claims which rely on allowable independent claim 1.

With regard to the 35 USC 103(a) rejection of claim 10 over LaChance, Olson, and Chang, applicant notes that LaChance describes a system where a CPU first executes bootstrap instructions from a ROM (col 1 lines 58-61), whereas claim 10 recites a system where the CPU first starts executing instructions in a memory which has been initialized by contents moved from a ROM by a ROM controller in combination with a DMA controller during an interval when the CPU is in an inactive state. The DMA controller of Olson is similarly initialized by the CPU (col 1 lines 48-50). Applicant notes that neither the initialization of the DMA controller by the ROM controller while the CPU is inactive in claim 10 first step, nor the movement of a boot image from the ROM to the memory while the CPU is inactive in claim 10 second step are found in

any combination of LaChance, Olson, or Chang, as these systems actively use the CPU for operations described in the third step and fourth step of claim 10.

With regard to the 35 USC 103(a) rejection of claim 12, applicant has amended this claim, notes that this proper dependant claim relies on allowable claim 10.

With regard to the 35 USC 103(a) rejection of claim 2, 4, 11 over LaChance, Olson, Chang, and Bashford 6,529,989, applicant notes the claims 2, 4, and 11 are proper dependant claims which rely on allowable claims 1 and 10.

With regard to the 35 USC 103(a) rejection of claims 8-9, 13-17, and 19 over LaChance, Olson, Chang, and Wiedeman 6,985,454, applicant notes that the system of Wiedman uses TCP/IP packets (col 9 lines 46-48), also known as a layer 3 protocol which includes retransmission, which provides for automatic retransmission of specific lost packets through the use of a mechanism using a range of sequence numbers. As is known in the art, the retransmission mechanism for individual lost packets requires a complicated tracking of packets sent and received using "windows", or ranges of TCP sequence

numbers. Claims 8, 9, 13-15, 17, 18, and 19 recite a system which does not retransmit individual packets - each packet is instead sent a plurality of times in the hope that at least one packet from each set of identical duplicates will be correctly received, whereas Wiedeman describes a system where each packet is sent once, unless a retransmission is requested, in which case only the missing packet is retransmitted. Applicant notes that this retransmissions also changes the sequence numbers of future packets sent by Wiedeman, in contrast with applicant's system sends the packets a fixed number of times, each duplicate packet having the identical sequence number as the original. Applicant's system of transmission of original and duplicate packets, each original and duplicate sent with identical sequence numbers and without regard to the correct or incorrect reception, and ignoring of identical sequence numbers on receipt is therefore distinguishable from Wiedman's TCP/IP system. Additionally, applicant's dependent claims 8-9, 13-15, and 17-19 are proper dependant claims which rely on allowable amended claims 1 and 10.

With regard to the 35 USC 103(a) rejection of claim 18 over LaChance, Olson, Chang and Schuster 6,170,075,

applicant notes that proper dependant claim 18 relies on allowable independent claim 10.

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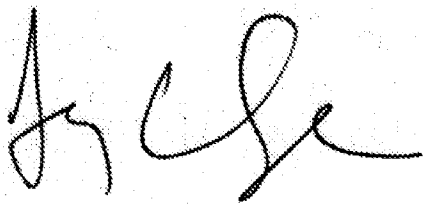
With this amendment, the present application is in condition for first office action.

Respectfully Submitted,



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